

Cascaded H-Bridge Inverter with Reduced Number of Switches and A Single Dc Source

1 Deepti Priyanka Behera, 1 A.K. Pradhan 1 Jyoti Prakash Jena, 1 Ashis Kumar Mallick 1
Girija Sankar Dora, 2 Debdas Sahoo

1 Asst. Prof. Department of Mechanical Engineering, Gandhi Institute of Technology &
Management, Bhubaneswar

2 Student of Department of Electrical & Electronics Engineering GITAM, BHUBANESWAR
ODISHA

ABSTRACT

This paper presents topology for a seven-level cascaded H-bridge multilevel inverter. The proposed topology uses reduced number of switches and requires only one DC source. The new topology results in reduced cost and can be enforced for any number of levels. The switching angles are generated using a new technique of selective harmonic elimination technique. The simulated waveforms of output voltage have reduced total harmonic distortion.

Introduction

Multilevel inverters (MLIs) are used in renewable energy applications for boosting low source voltage to higher voltage for transmitting power to loads and/ or to grid. They have higher efficiency, lower electromagnetic interference effects, lower switching losses, reduced output dv/dt , higher voltage capability and better power quality as compared to conventional inverters [1-3]. There are mainly three configurations of MLIs namely Cascaded H-Bridge, Neutral Point Clamped and Flying Capacitor. Of these the Cascaded H Bridge (CHB) Inverter has proven to be a better for high power applications as it does not require any clamping diodes or capacitors. It achieves the high voltage by cascading multiple single-phase inverter modules and requires the least number of components [4-6]. The main disadvantage of the CHB inverter is that it requires a separate DC source for each level. This paper proposes a new topology for CHB MLI, which uses only two switches per level and a single DC source. There must be a separate DC source for every individual H-bridge [7]. Fig. 1 shows the conventional

single- phase seven-level CHB. The inverter uses three separate DC sources. Each DC source has a single-phase H-bridge inverter with four switches resulting in twelve switches for the seven-level inverter. Fig. 2 shows the output voltage waveform of the seven-level inverter. The seven levels of voltages are 0, $\pm VDC$, $\pm 2VDC$ and $\pm 3VDC$.

Proposed Multi-Level Inverter Fig. 3 shows the proposed topology of the CHB multi-level inverter. It requires only one DC source. It uses 3 three winding transformers with two primary windings and one secondary winding. The DC source is connected to the primary side of the transformer through the switches. The secondary windings are connected in series and the load is connected to the series connected secondary windings. The switching states to get the seven levels of output voltages are given in Table 1.

Switching Signals for the Inverter using Selective Harmonic Elimination Method The multi-level output AC voltage is obtained by switching ON and OFF the

semiconductor switches in such a way that the desired fundamental is obtained with less harmonic distortion. The commonly used technique for switching is the selective harmonic elimination (SHE) method at fundamental frequency. In this technique, the switching angles are computed by solving transcendental equations characterizing harmonics [8-9]. For a seven-level inverter, three switching angles are to be generated by solving three transcendental equations. This method solves the transcendental equations with initial approximate values [10].

The output voltage waveform of the seven-level inverter is shown in Figure 4 for a half-cycle. Where θ_1 , θ_2 , and θ_3 are the switching angles for the seven-level inverter, and $0 < \theta_1 < \theta_2 < \theta_3 < \pi/2$. From Eqn.1 the fundamental voltage is given by Eqn. 2

The maximum value of the fundamental voltage is $\frac{m}{4} V_{DC}$, i.e., the value of the fundamental voltage when all the switching angles are zero. Modulation index M is the ratio of fundamental voltage to maximum fundamental voltage. $M = \frac{V_1}{\frac{m}{4} V_{DC}}$. Three equations can be formed to eliminate third and fifth harmonics Solving these equations by Newton-Raphson method, the switching angles are found to be $\theta_1 = 0.2923$ rad, $\theta_2 = 0.5542$ rad, $\theta_3 = 1.377$ rad for $M = 2$. Hence the third and fifth harmonics are eliminated, while higher order harmonics are present. This paper considers a new technique for the minimisation of seventh harmonic in addition to the elimination of third and fifth harmonics. This is implemented by considering seventh harmonic equation along with the third and fifth harmonic equations and ignoring the fundamental one. Seventh harmonic cannot be completely eliminated but can be minimised by equating Eqn. 6 to a minimum value. By solving Eqns. 4-6 by

Newton-Raphson method, the values of switching angles are found to be $\theta_1 = 0.2037$ rad, $\theta_2 = 0.4701$ rad, $\theta_3 = 0.9784$ rad

Result:

Fig. 5 Output voltage and FFT analysis of proposed CHB inverter with the conventional SHE technique

Comparison of Various Parameters of Seven-Level Conventional and Proposed CHB inverter

Conclusion

This paper presented a new topology for CHB inverter which uses less number of switches compared to conventional multi-level inverter. The proposed inverter uses only $(m-1)$ switches whereas the conventional inverter uses $2(m-1)$ switches, for an m -level inverter. In addition, the proposed topology uses only one DC source irrespective of the levels but the conventional one uses $(m-1)/2$ DC sources for multilevel resulting in reduced cost of the proposed inverter. This paper also presented a new technique for SHE, which involves the elimination of third and fifth and reduction of seventh harmonics resulting in lower THD value.

References

- [1] Jih-Sheng Lai and Fang Zheng Peng, "Multilevel Converters - A New Breed of Power Converters", IEEE Transactions on Industry Applications, Vol. 32, No. 3, pp. 509- 517, May/June 1996.
- [2] F. Z. Peng, J. W. McKeever and D. J. Adams, "Cascade Multilevel Inverters for Utility Applications", IECON Proceedings (Industrial Electronics Conference), Vol. 2, pp. 437-442, 1997.
- [3] B. Urmila and D. Subbarayudu, "Multilevel Inverters: A Comparative Study of Pulse Width Modulation Techniques", International Journal of Scientific &

Engineering Research, Vol. 1, No. 3, pp. 1-5, December 2010.

[4] José Rodríguez, Jih-Sheng Lai and Fang Zheng Peng, “Multilevel Inverters: A Survey of Topologies”, IEEE Transactions on Controls and Applications, Vol. 49, No. 4, August 2002.

[5] G. Carrara, S. Gardella, M. Marchesoni, R. Salutari and G. Sciutto, “A New Multilevel PWM Method: A Theoretical Analysis”, IEEE Transactions on Power Electronics, Vol. 7, No. 3, pp.497-505, July 1992.

[6] T. Prathiba and P. Renuga, “A comparative study of Total Harmonic Distortion in Multi-level inverter topologies”, Journal of Information Engineering and Applications, Vol. 2, No.3, pp. 26-36, 2012.

[7] Fang Zheng Peng, Jih-Sheng Lai, J. McKeever and J. Van Coevering, “A Multilevel Voltage-Source Inverter with Separate DC Sources for Static Var Generation”, IEEE Trans. on Industry Applications, Vol. 32, No. 5, pp. 1130-1138, September/October 1996.

[8] Jose Rodriguez, J S Lai, and F. Z. Peng, “Multilevel Inverters: A Survey of Topologies, Controls, and Applications”, IEEE Trans. on Industrial Electronics, vol. 49, no. 4, pp. 724- 738, August 2002.

[9] C. Woodford and C. Phillips, “Numerical Methods with Worked Examples”, Chapman and Hall, pp. 45-57, First edition 1997.